IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of Johansson <i>et al.</i>	;))
Serial No.: 10/699,222 Filed: October 31, 2003 For: Semiconductor process and integrated circuit		 PATENT PENDING Examiner: H. Jey Tsai Group Art Unit: 2812
		Attorney's Docket No: 5497-105
MS AMENDMENT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450	I hereby de sheen for trace St April 16	certify that this correspondence is being: eposited with the United States Postal Service on the date own below with sufficient postage as first class mail in an evelope addressed to: Mail Stop Amendment, Commissioner Patents, P.O. Box 1450, Alexandria, VA 22313-1450. ensmitted by facsimile on the date shown below to the United ates Patent and Trademark Office at (571/273/8390. 6, 2008 Date Kathleen L. McDermott respondence is being: ectronically submitted via EFS-Web

Dear Sir:

In reply to the Office Action of March 26, 2008 imposing a restriction requirement,
Applicant elects Group II claims, that is claims 1 through 35. This election is made without traverse.

Respectfully submitted,

COATS & BENNETT, P.L.L.C.

Dated: April 16, 2008

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